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Bib Data Sheet

CONFIRMATION NO. 4856

SERIAL NUMBER 10/762,340	FILING DATE 01/23/2004  RULE	CLASS 257	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 247958US2
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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-087822 03/27/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/26/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>My</u> Examiner's Signature <u>My</u> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 6
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## TITLE

Semiconductor device capable of avoiding latchup breakdown resulting from negative variation of floating offset voltage

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